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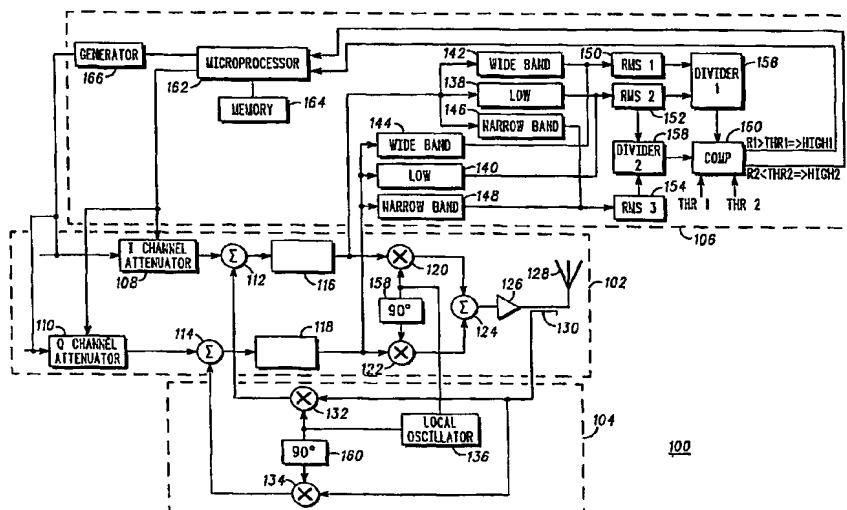
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(54) Title: **CARTESIAN LOOP TRANSMITTER AND METHOD OF ADJUSTING AN OUTPUT LEVEL OF SUCH TRANSMITTER**



(57) Abstract: In accordance with the present invention there is those provided a Cartesian loop transmitter (100) having an isolator eliminator circuitry (106) comprising a set of low pass (138, 140) and wide band pass (142, 144) as well as narrow band pass (146, 148) filters for each of I- and Q-channels, root mean square detectors (152, 154, 156) and dividers (156, 158) connected to a means for comparing (160). Signals from said means for comparing (160) are received by a microprocessor (162) which controls attenuation setting. There is also provided a method of adjusting an output level of such transmitter (100). Said method comprises the step of generating a small signal (200) measuring an on-channel signal level (212) and said small signal level (214, 216) and then calculating a first ratio (218) of said small signal to said on-channel signal. If said first ratio exceeds a defined threshold (220) said attenuation setting is increased (224).



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